

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph on page 11, lines 7-26 with the following amended paragraph:

Following the falling ramp waveform Rdn, a rising ramp waveform Rup, which is rising from $-V_1$ to zero(0) V or the ground voltage GND, is simultaneously applied to both of the scan electrodes Y1 to Y_n and the sustain electrodes Z. At this time, the address electrodes X1 to X_m are maintained at zero(0) V or the ground voltage GND. When the rising ramp waveform Rup is applied as set forth above, the set-down discharge is occurred in the dark discharge type between the scan electrodes Y1 to Y_n and the address electrodes X1 to X_m , and between the sustain electrodes Z and the address electrodes X1 to X_m . By the set-down discharge, excessive wall charges unnecessary for the address discharge are eliminated. As the result, the wall charges needed for the address discharge ~~are uniformly remained~~ remain within all of the cells. The distribution of wall charges accumulated when the reset period is ended is as follows. The negative wall charges ~~are remained~~ remain on the address electrodes X, whereas the positive wall charges ~~are uniformly remained~~ remain on the scan electrodes Y1 to Y_n and the sustain electrodes Z.

Please replace the paragraph on page 13, lines 19-33 with the following amended paragraph:

In the reset period, a falling ramp waveform Rdn, which is falling from the negative voltage $-V_1$, is simultaneously applied to all of the scan electrodes Y1 to Y_n and the sustain electrodes Z. At the same time, zero(0)V or the ground voltage GND is applied to the address electrodes

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X₁ to X_m. By the falling ramp waveform R_{dn}, a set-up discharge is concurrently occurred between the scan electrodes Y₁ to Y_n and the address electrodes X₁ to X_n and between the sustain electrodes Z and the address electrode X₁ to X_m within the cells of the full screen. By the set-up discharge, positive wall charges are accumulated on the scan electrodes Y₁ to Y_n ~~as~~ as shown in ~~Figs~~ Figs. 10 and 11, whereas, negative wall charges are accumulated on the address electrodes X and the sustain electrodes Z.